ADDI-DATA®
SPIRIT OF EXCELLENCE

TECHNICAL

DESCRIPTION

PX901

Screw terminal panel for DIN rail

DIN EN ISO 9001:2008 certified

Edition: 10.02-11/2014

Product information

This manual contains the technical installation and important instructions for correct commissioning and usage, as well as production information according to the current status before printing.
The content of this manual and the technical product data may be changed without prior notice.
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Warning!

The following risks result from the improper implementation of the screw terminal panel and from use contrary to the regulations:

Personal injury

Damage to the screw terminal panel, the PC and peripherals

Pollution of the environment.

- Protect yourself, others and the environment!
- Read the safety precautions (yellow leaflet) carefully!

  If this leaflet is not enclosed with the documentation, please contact us and ask for it.
- Observe the instructions of this manual!

  Make sure that you do not forget or skip any step!
  We are not liable for damages resulting from the wrong use of the screw terminal panel.
- Pay attention to the following symbols:

NOTICE!
Designates hints and other useful information.

NOTICE!
Designates a possibly dangerous situation.
If the instructions are ignored, the screw terminal panel, the PC and/or peripherals may be **destroyed**.

WARNING!
Designates a possibly dangerous situation.
If the instructions are ignored, the screw terminal panel, the PC and/or peripherals may be **destroyed** and persons may be **endangered**.

Contents

Figures

Tables

Chapter overview

In this manual, you will find the following information:

Chapter	Content
1	Important information on the application and the user of the screw terminal panel
2	Description of the different versions of the screw terminal panel
3	Detailed information on the connection of the screw terminal panel to the PC boards including terminal assignments and jumper positions
4	Connection examples with different PC boards
5	Procedure for returning or disposing of the screw terminal panel
6	List of technical data and limit values of the screw terminal panel
7	Appendix with glossary and index
8	Contact and support address

1 Definition of application, user, handling

1.1 Definition of application

1.1.1 Intended use

The screw terminal panel **PX901** is used as electrical equipment for electrical measurement, control and laboratory pursuant to the norm EN 61010-1 (IEC 61010-1).

1.1.2 Usage restrictions

The screw terminal panel **PX901** must not be used as safety related parts (SRP).

The screw terminal panel **PX901** must not be used in potentially explosive atmospheres.

1.1.3 Limits of use

All safety information and the instructions in the manual must be followed to ensure proper intended use.

Uses of the screw terminal panel beyond these specifications are considered as improper use.
The manufacturer is not liable for damages resulting from improper use.

The screw terminal panel must remain in its anti-static packaging until it is installed.

Please do not delete the identification numbers of the screw terminal panel or the warranty claim will be invalid.

1.2 User

1.2.1 Qualification

Only persons trained in electronics are entitled to perform the following works:

- Installation
- Commissioning
- Use
- Maintenance.

1.2.2 Country-specific regulations

Do observe the country-specific regulations regarding

- the prevention of accidents
- electrical and mechanical installations
- Electromagnetic compatibility (EMC).

1.3 Questions and updates

If you have any questions, you can send them to us by e-mail or call us:

E-mail: info@addi-data.com

Phone: +49 7229 1847-0.

Manual and software download from the nternet

The latest version of the technical manual for the screw terminal panel **PX901** can be downloaded for free at: www.addi-data.com

NOTICE!

Before using the board or in case of malfunction during operation, check if there is an update available on our website (manual, etc.) or contact us directly.

2 Layout

The screw terminal panel **PX901** is used to connect up to 32 signal lines or signal reference lines.

2.1 PX901-DG

All digital 24 V signals from the 37-pin D-Sub connector are led to the two rows of screw terminals via a status LED. The voltage supply (24 V and GND for digital output boards or input/output boards; digital GND for digital input boards) is led to a separate screw terminal unit.

The screw terminal panel **PX901-DG** can be connected to the following PC boards and MSX-E systems with **digital** signals:

Table 2-1: Boards and MSX-E systems with PX901-DG

PCI Express boards	APCIe-1502, APCIe-1516, APCIe-1532, APCIe-1564
PCI boards	APCI-1016, APCI-1032, APCI-1500, APCI-1516, APCI-1564, APCI-2016, APCI-2032
CompactPCI boards	CPCI-1500, CPCI-1564
CompactPCI Serial boards	CPCIs-1532, CPCIs-1564
ISA boards	PA 1000, PA 1500, PA 2000
PC/104-PLUS boards	PC104-PLUS1500
MSX-E systems	MSX-E3121, MSX-E3701-DIO

Fig. 2-1: PX901-DG: Layout diagram

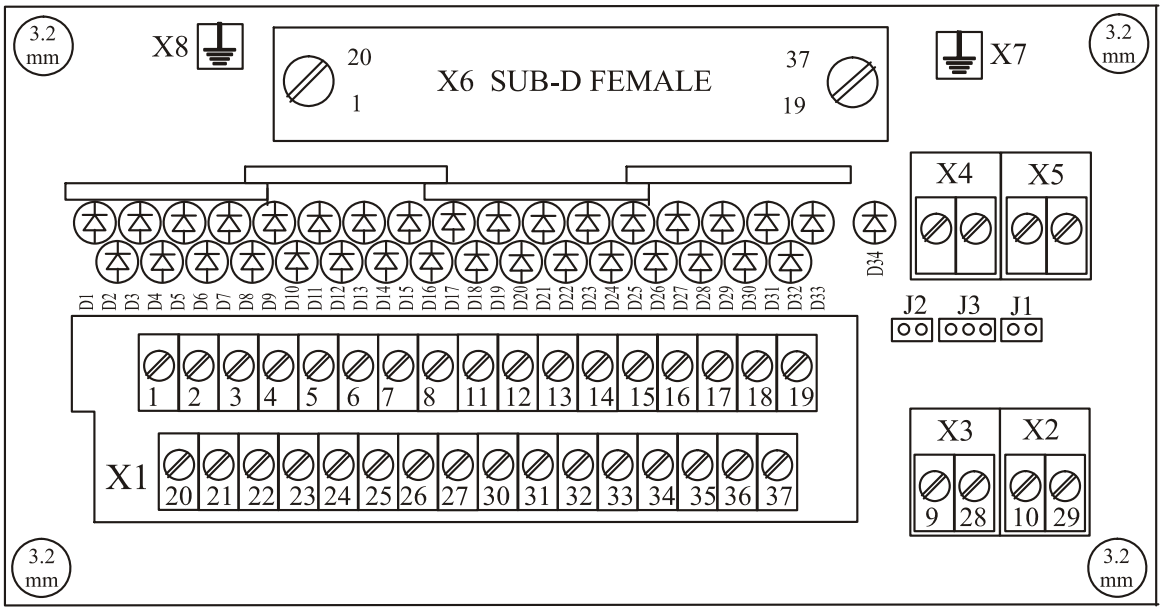
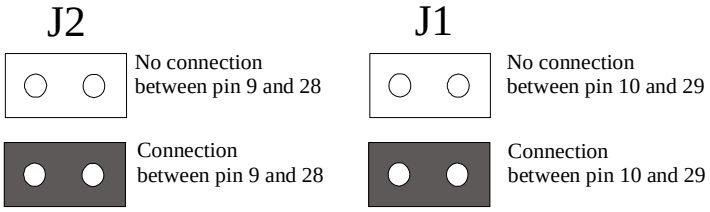


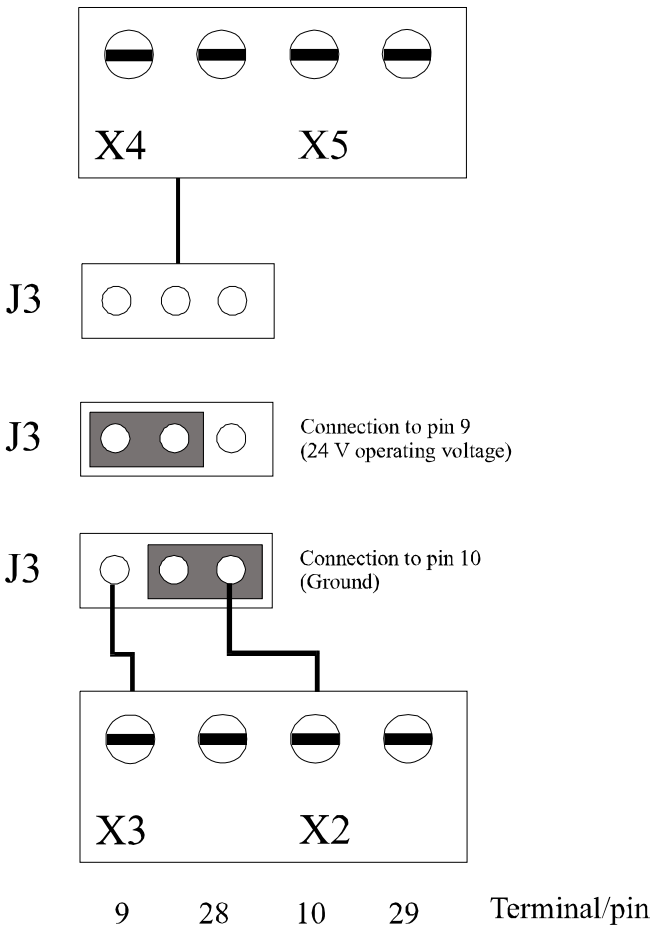
Table 2-2: PX901-DG: Layout description

D1-D33	Red LEDs for the status display of the 24 V signals D1 = Status LED for digital 24 V signal at screw terminal / connector pin 1 D2 = Status LED for digital 24 V signal at screw terminal / connector pin 20 D3 = Status LED for digital 24 V signal at screw terminal / connector pin 2 D4 = ... The lower row of LEDs corresponds to the lower row of the screw terminals. In the same way, the upper row of LEDs corresponds to the upper row of the screw terminals.
D34	Green LED for status display when connecting the 24 V operating voltage to screw terminal pin 9
X1	Two rows of screw terminals (33-pin) for the connection of the digital 24 V signals Each terminal is assigned a pin of the 37-pin female connector X6. The pin number is indicated directly at the screw terminal of the printed circuit board.
X2, X3	Please see the specific pin assignment of your PC board! The screw terminals X3 and X2 have different functions depending on the board with which you want to use the screw terminal panel **PX901-DG**. For the respective terminal assignments, see Chapter 3.
X4, X5	Additional screw terminals for the distribution of the 24 V operating voltage or the GND. You can select them using jumper J3. The screw terminals X4 and X5 have different functions depending on the board with which you want to use the screw terminal panel **PX901-DG**. For the respective terminal assignments, see Chapter 3.
X6	37-pin D-Sub female connector for the connection of the connection cable **ST010** or **ST011**
X7, X8	Screw terminals for the ground connection

Table 2-3: Jumper positions

J2, J1	On a few of ADDI-DATA PC boards, several pins of the peripheral connector are joined to distribute the energy the board is supplied with to several leads of a connection cable. Please see the Technical Description of the respective board and the corresponding terminal assignment in Chapter 3. The selection is made through jumpers J1 and J2.  J1 set: Connection between pin 10 and pin 29 (e.g. for digital I/O boards) J1 not set: No connection between pin 10 and pin 29 J2 set: Connection between pin 9 and pin 28 J2 not set: No connection between pin 9 and pin 28.

J3	Extension of terminals X2 and X3 through jumper J3



2.2 PX901-AG

All analog signals from the 37-pin D-Sub connector are led to the two rows of screw terminals via voltage protection diodes. Overvoltages > 13 V are diverted to the shield of the connector housing or to the connection terminals X7 / X8.
For the connection of the signal GND, separate screw terminal units (X2, X3, X4, X5) are available. According to the board used, various additional signals (+5 V PC output: Trigger input, etc.) are available at the terminals.

The screw terminal panel **PX901-AG** can be connected to the following PC boards with **analog** signals:

Table 2-4: Boards with PX901-AG

PCI Express boards	APCIe-3021, APCIe-3121, APCIe-3123, APCIe-3521
PCI boards	APCI-3001, APCI-3002, APCI-3003, APCI-3010, APCI-3016, APCI-3110, APCI-3116, APCI-3120, APCI-3501
CompactPCI boards	CPCI-3001, CPCI-3009, CPCI-3120
CompactPCI Serial boards	CPCIs-3121
ISA boards	PA 302, PA 311

Fig. 2-2: PX901-AG: Layout diagram

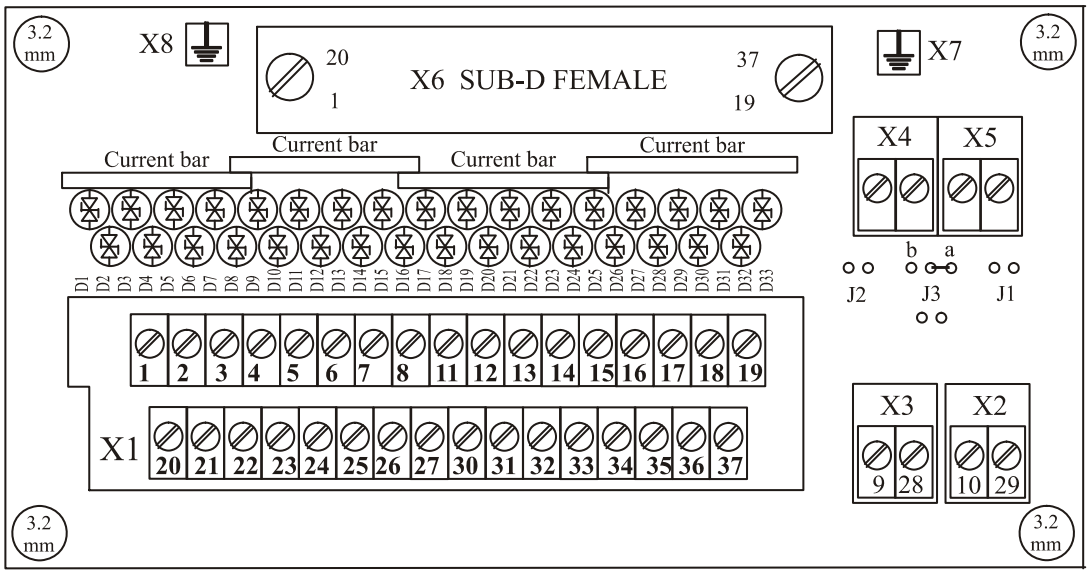


Table 2-5: PX901-AG: Layout description

D1-D33	Diodes for overvoltage protection of the analog inputs/outputs
X1	Two rows of screw terminals (33-pin) for the connection of the analog signals Each terminal is assigned a pin of the 37-pin female connector X6. The pin number is indicated directly on the screw terminal of the circuit board.
X2	Double terminals for the connection of the analog signals to pins 10 and 29 Please see the specific pin assignment of your PC board!
X3	Double terminal for the connection of the analog signals to pins 9 and 28 Please see the specific pin assignment of your PC board!
X4, X5	Terminals are connected electrically to pin 10 of the 37-pin female connector X6 or terminal X2
X6	37-pin D-Sub female connector for the connection of the connection cable **ST010** or **ST011**
X7, X8	Screw terminals for the ground connection

2.3 PX901-ZG

The screw terminal panel **PX901-ZG** can be connected to the following PC boards:

Table 2-6: Boards with PX901-ZG

PCI Express boards	APCIe-2200, APCIe-3021, APCIe-3121, APCIe-3123, APCIe-3521	Connection of the digital I/O
	APCIe-1711	Connection of the signals for the “Sin/Cos” function
PCI boards	APCI-2200, APCI-3001, APCI-3002, APCI-3003, APCI-3120, APCI-3200, APCI-3300, APCI-3501, APCI-3701, APCI-3702	Connection of the digital I/O
CompactPCI boards	CPCI-3001, CPCI-3009, CPCI-3120	Connection of the digital I/O
CompactPCI Serial boards	CPCIs-3121	Connection of the digital I/O
	CPCIs-1711	Connection of the signals for the “Sin/Cos” function
ISA boards	PA 1700-2	Connection of the counter signals

Fig. 2-3: PX901-ZG: Layout diagram

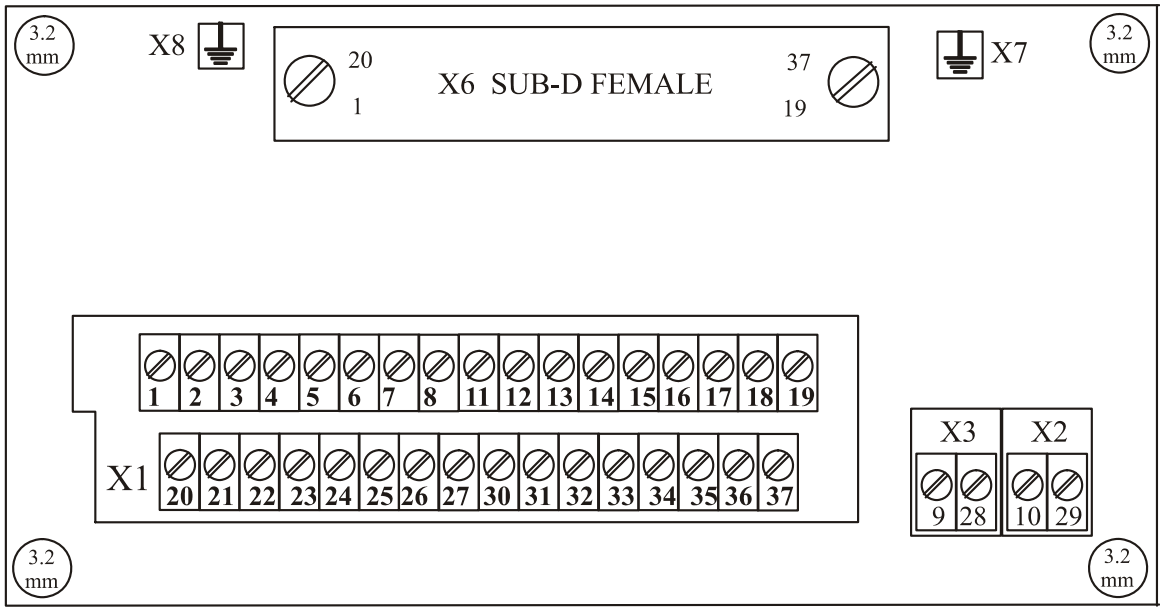


Table 2-7: PX901-ZG: Layout description

X1	Two rows of screw terminals (33-pin) for screwed connection Each terminal is assigned a pin of the 37-pin female connector X6. The pin number is indicated directly at the screw terminal of the printed circuit board.
X2, X3	Please see the specific pin assignment of your PC board!
X6	37-pin D-Sub female connector for the connection of the connection cable **ST010** or **ST011**
X7, X8	Screw terminals for the ground connection

3 Connection to the PC boards

Risk of injury!
Please follow the safety precautions!
An improper handling of the screw terminal panel may cause property damage and injury.

Using the 37-pin D-Sub female connector and the standard cable **ST010** or **ST011**, the screw terminal panel **PX901** can be connected to digital, analog and counter boards as well as to the Ethernet systems **MSX-E3121** and **MSX-E3701-DIO**. In terms of electromagnetic compatibility (EMC), the standard cable has the following properties:

- Metallised connector housing
- Shielded cable
- Cable shield folded back over insulation and firmly screwed on both sides to the connector housing.

The housing of the female connector is connected with two ground terminals which allow for additional grounding of the screw terminal panel. All components of the screw terminal panel are enclosed in a ground strap that is also connected to the ground terminals.

Each terminal of the screw terminal panel is directly connected to a pin of the 37-pin D-Sub female connector and can hold a conductor cross-section up to 2.5 mm². According to the ADDI-DATA board used, the terminals have different functions.

NOTICE!
When operating the board **APCI-/CPCI-3120**, **PA 3xx** or **PA 1700-2**, make sure that no external 24 V voltage is connected, as otherwise, the board might be destroyed.

The different possibilities of the connection to the boards are shown in Chapter 4.

3.1 Pin and terminal assignments

On the screw terminal panel, the pins of the 37 pin D-Sub female connector and the terminals connected to them are numbered in the same way. Thus, the terminal assignment of the screw terminal panel is identical with the pin assignment of the PC board.

Table 3-1: Pin and terminal numbers

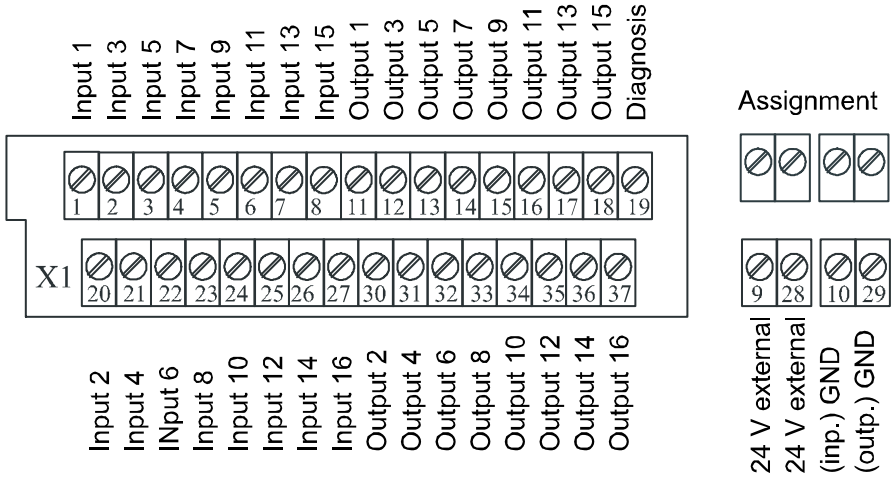
Pin No.	Terminal No.
1	1
2	2
...	...
37	37

3.2 Connection of the PX901-DG

3.2.1 Connection to the APCIe-1502, APCIe-1532 and APCI-/CPCI-1500 as well as to the Ethernet systems MSX-E3121 and MSX-E3701-DIO

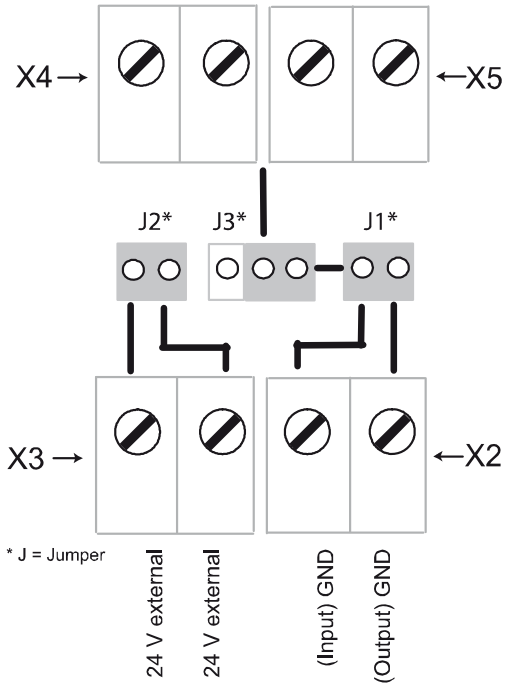
a) Terminal assignment

Fig. 3-1: Terminal assignment with the APCIe-1502, APCIe-1532 and APCI-/CPCI-1500 as well as with the Ethernet systems MSX-E3121 and MSX-E3701-DIO



b) Jumper position

Fig. 3-2: Example: Jumper position with the APCIe-1502, APCIe-1532 and APCI-/CPCI-1500 as well as with the Ethernet systems MSX-E3121 and MSX-E3701-DIO



The reference ground of the digital inputs and outputs is on the same potential.

Jumpers J2 and J3:

Additionally, four connection terminals for the connection of the reference ground are available.

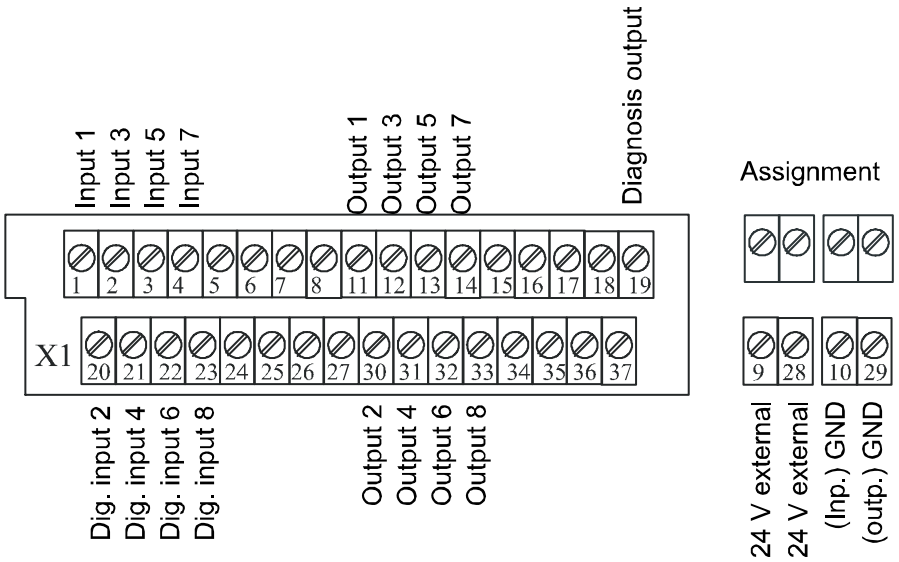
Jumper J1:

The current for the outputs is led to the board over two lines.

3.2.2 Connection to the APCIe-/APCI-1516

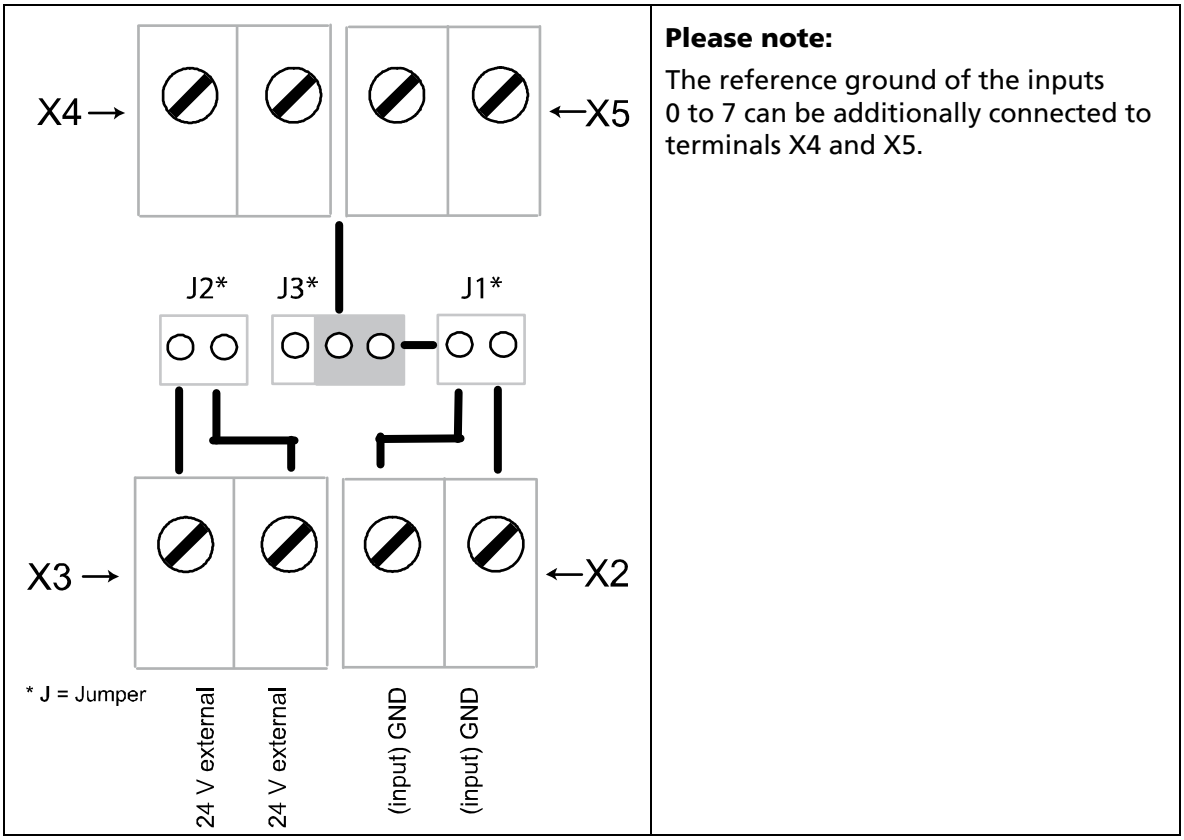
a) Terminal assignment

Fig. 3-3: Terminal assignment with the APCIe-/APCI-1516



b) Jumper position

Fig. 3-4: Example: Jumper position with the APCIe-/APCI-1516



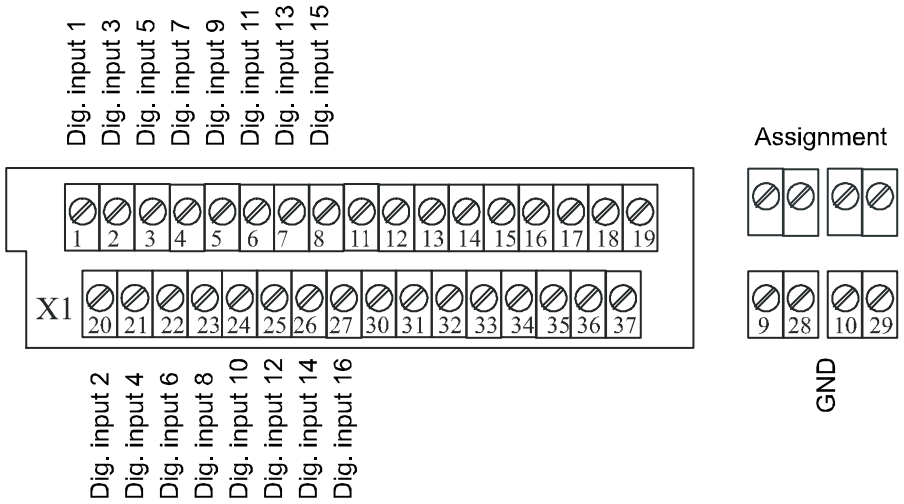
Please note:

The reference ground of the inputs 0 to 7 can be additionally connected to terminals X4 and X5.

3.2.3 Connection to the APCI-1016

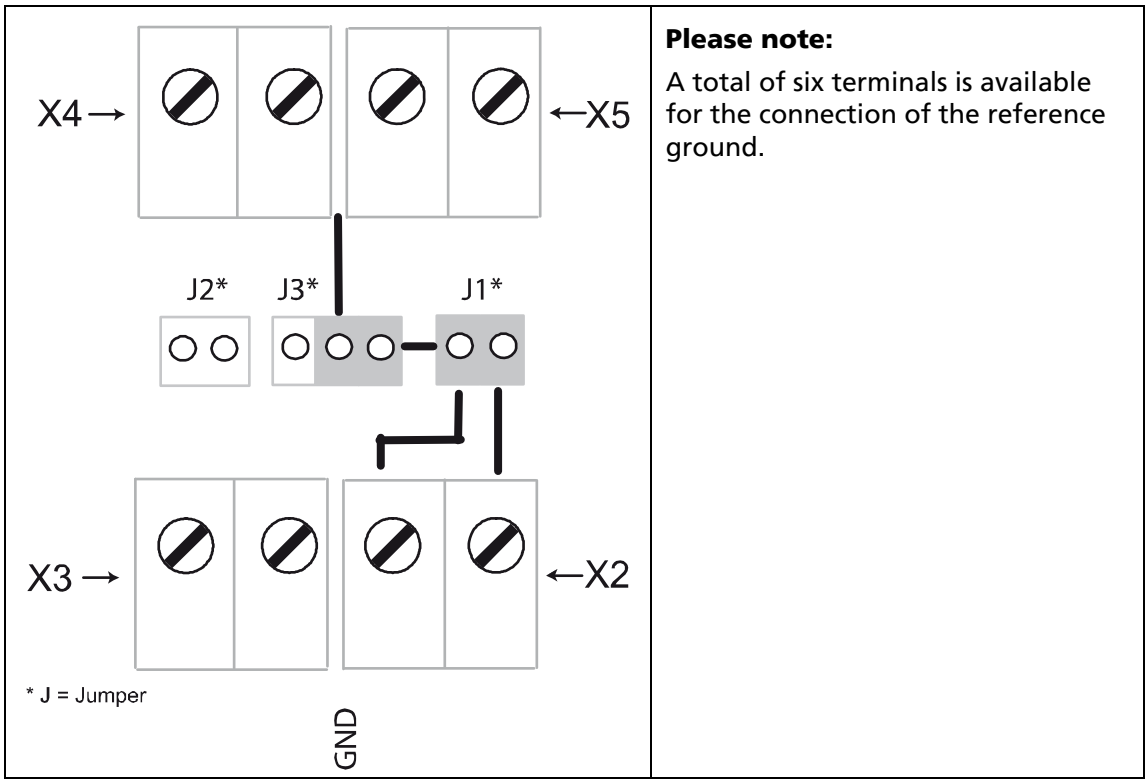
a) Terminal assignment

Fig. 3-5: Terminal assignment with the APCI-1016



b) Jumper position

Fig. 3-6: Example: Jumper position with the APCI-1016



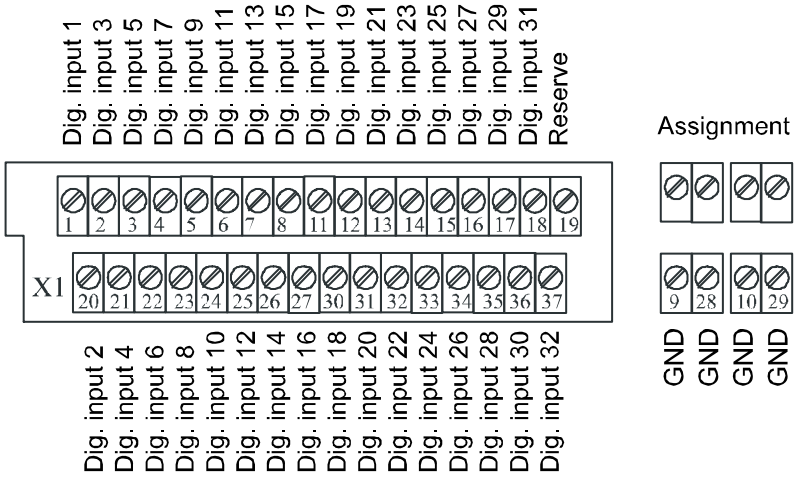
Please note:

A total of six terminals is available for the connection of the reference ground.

3.2.4 Connection to the APCIe-1564, APCI-1032, APCI-/CPCI-1564, CPCIs-1564 and PA 1000 (digital inputs)

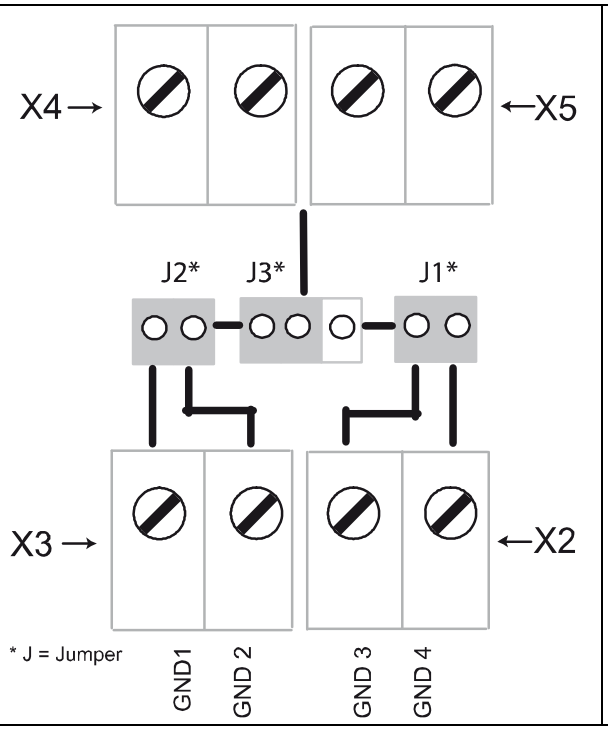
a) Terminal assignment

Fig. 3-7: Terminal assignment with the APCIe-1564, APCI-1032, APCI-/CPCI-1564, CPCIs-1564 and PA 1000



b) Jumper position

Fig. 3-8: Example: Jumper position with the APCIe-1564, APCI-1032, APCI-/CPCI-1564, CPCIs-1564 and PA 1000



Please note:

Inputs 0 to 15 are led to the same reference ground. Additionally, at terminals X4 and X5, further connection options are available for this reference ground.

Jumper position

GND 1 = GND 2

Reference ground of inputs 0-7 is connected to reference ground of inputs 8-15

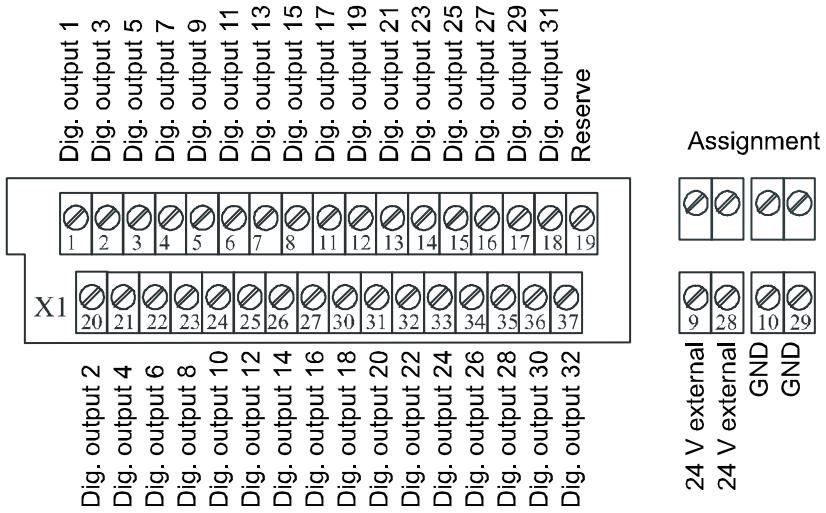
GND 3 = GND 4

Reference ground of inputs 16-23 is connected to reference ground of inputs 24-31

3.2.5 Connection to the APCIe-1564, APCI-1564, APCI-2032, CPCI-1564, CPCIs-1564 and PA 2000 (digital outputs)

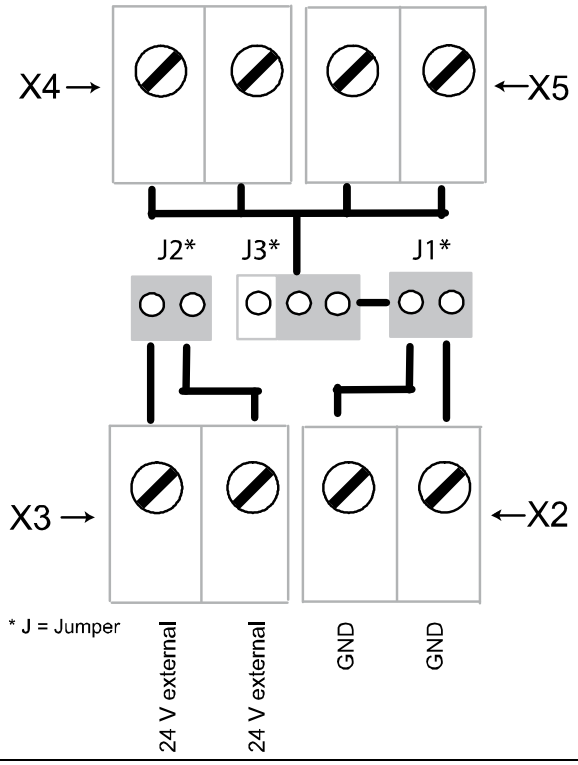
a) Terminal assignment

Fig. 3-9: Terminal assignment with the APCIe-1564, APCI-1564, APCI-2032, CPCI-1564, CPCIs-1564 and PA 2000



b) Jumper position

Fig. 3-10: Example: Jumper position with the APCIe-1564, APCI-1564, APCI-2032, CPCI-1564, CPCIs-1564 and PA 2000



Jumpers J1 and J3:

A total of six terminals is available for the connection of the reference ground of the outputs.

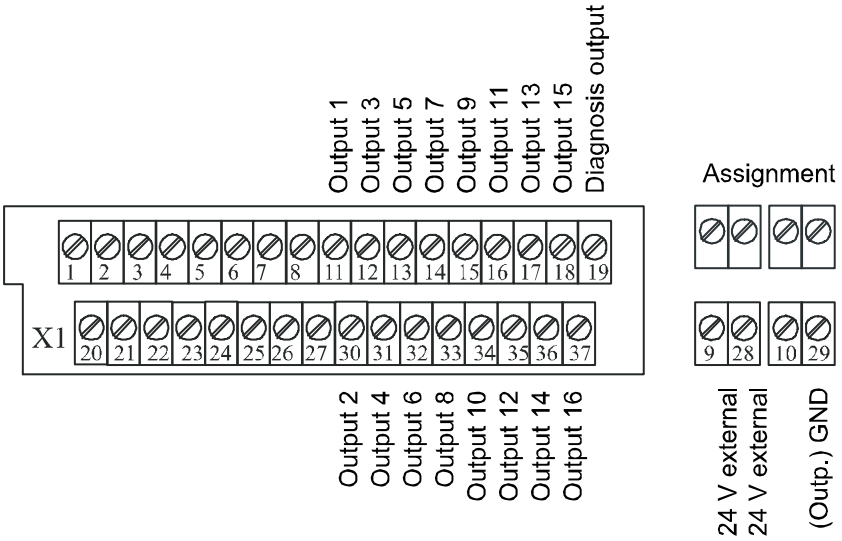
Jumper J2:

The current for the outputs is led to the board over two lines.

3.2.6 Connection to the APCI-2016

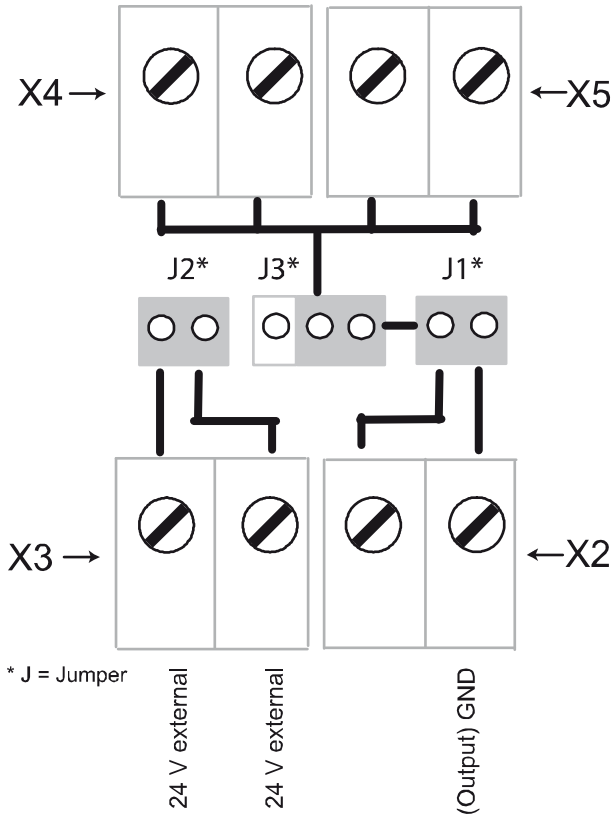
a) Terminal assignment

Fig. 3-11: Terminal assignment with the APCI-2016



b) Jumper position

Fig. 3-12: Example: Jumper position with the APCI-2016



Jumpers J2 and J3:

The reference ground of the outputs can be connected to terminals X2, X4 and X5.

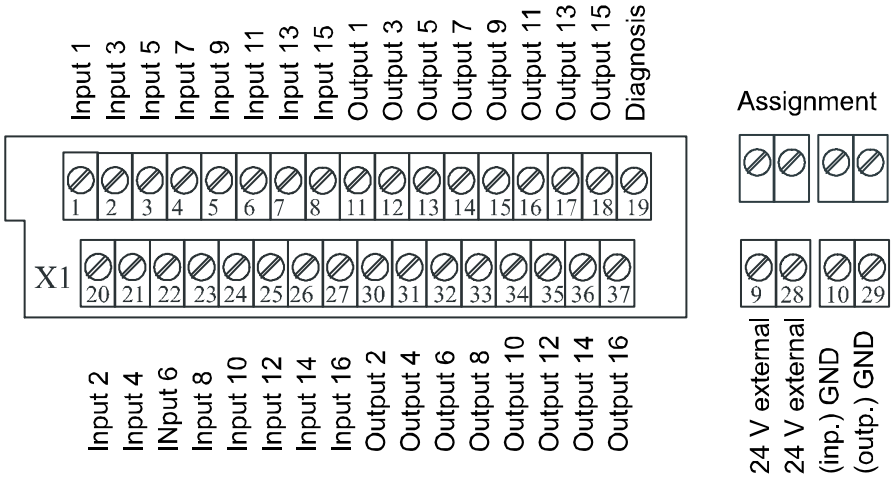
Jumper J1:

The current is supplied to the board over two lines.

3.2.7 Connection to the PA 1500

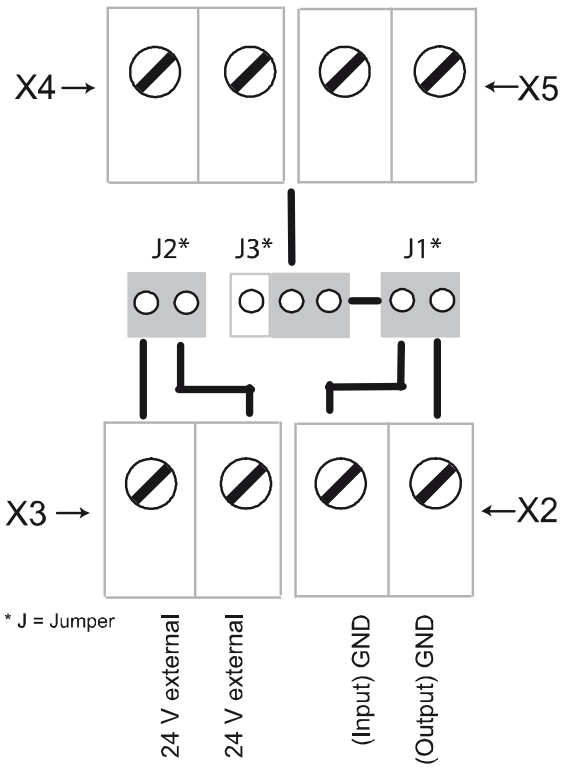
a) Terminal assignment

Fig. 3-13: Terminal assignment with the PA 1500



b) Jumper position

Fig. 3-14: Example: Jumper position with the PA 1500



The reference ground of the digital inputs and outputs is on the same potential.

Jumpers J2 and J3:

Additionally, four connection terminals for the connection of the reference ground are available.

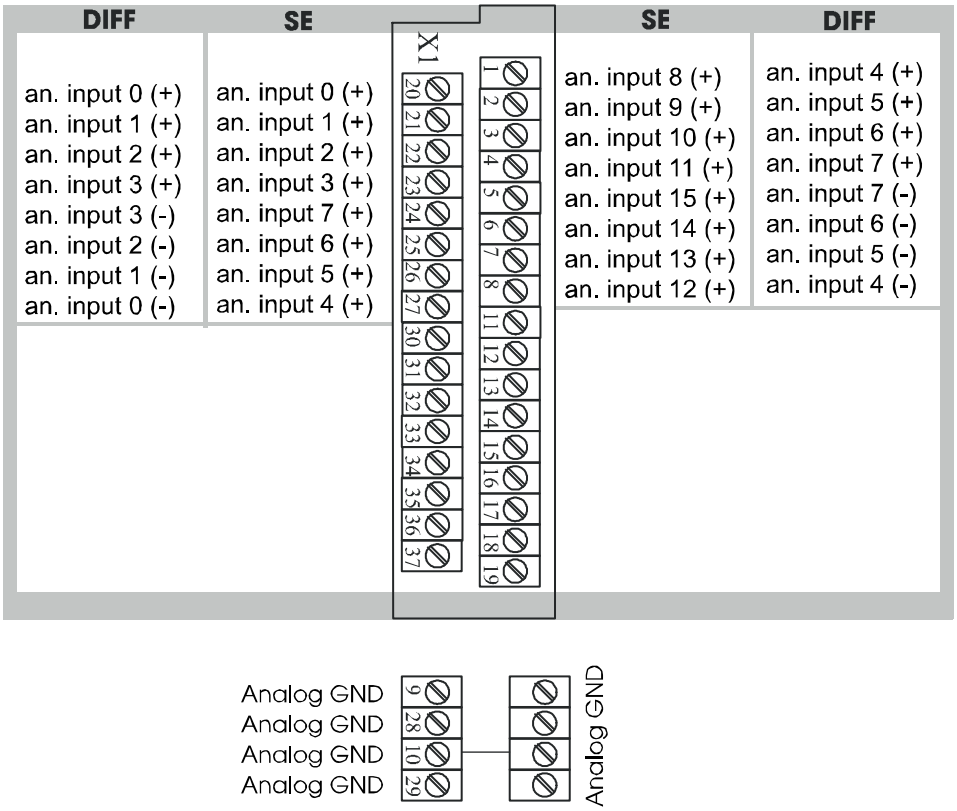
Jumper J1:

The current for the outputs is led to the board over two lines.

3.3 Connection of the PX901-AG

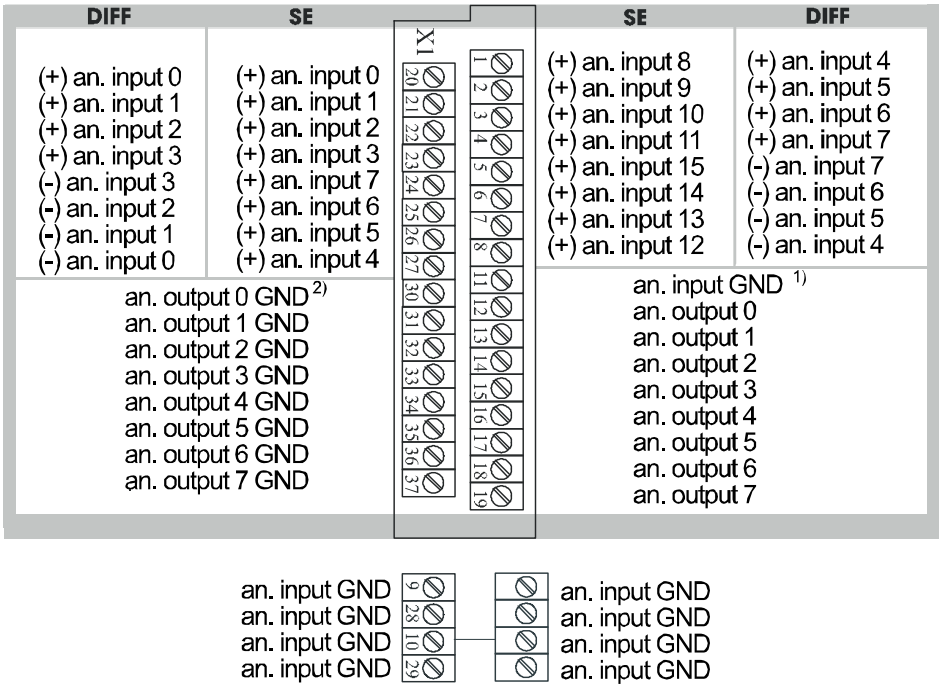
3.3.1 Connection to the APCIe-3021 and APCI-/CPCI-3001

Fig. 3-15: Terminal assignment with the APCIe-3021 and APCI-/CPCI-3001



3.3.2 Connection to the APCIe-3121, APCI-/CPCI-3120 and CPCIs-3121

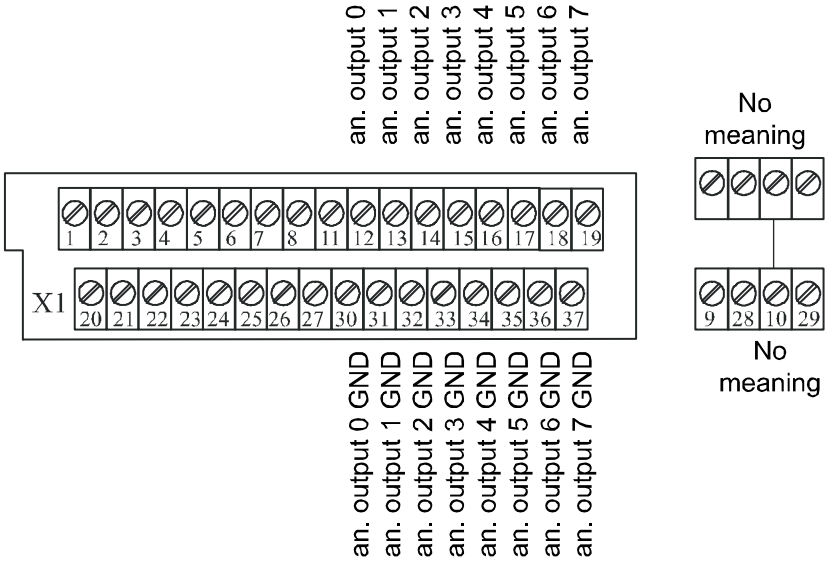
Fig. 3-16: Terminal assignment with the APCIe-3121, APCI-/CPCI-3120 and CPCIs-3121



1) Common ground line for the analog inputs
2) Analog single ground for the outputs, i.e. the same potential but with separate lines to the peripherals

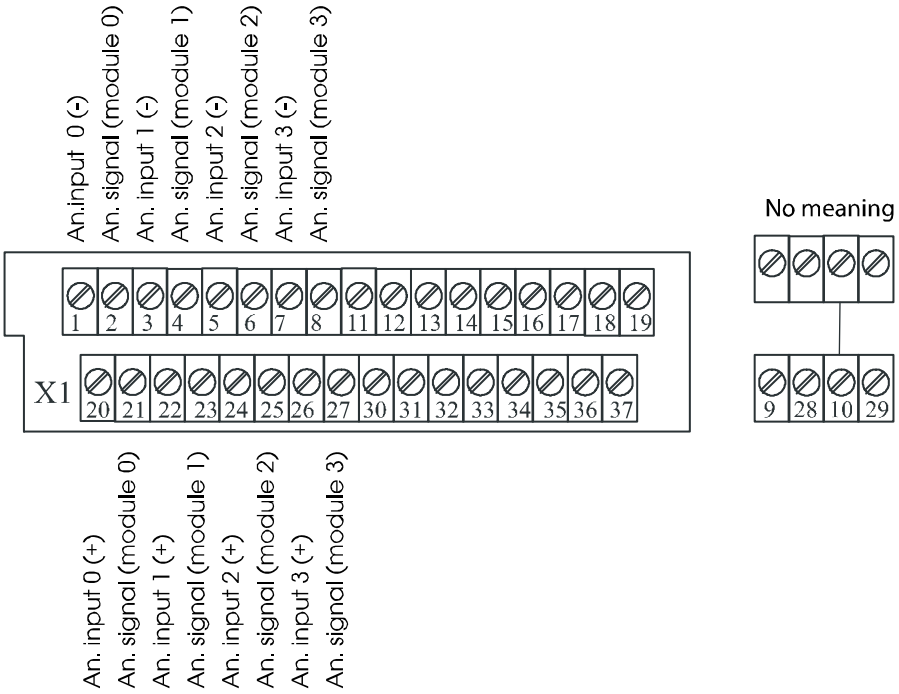
3.3.3 Connection to the APCIe-3521 and APCI-3501

Fig. 3-17: Terminal assignment with the APCIe-3521 and APCI-3501



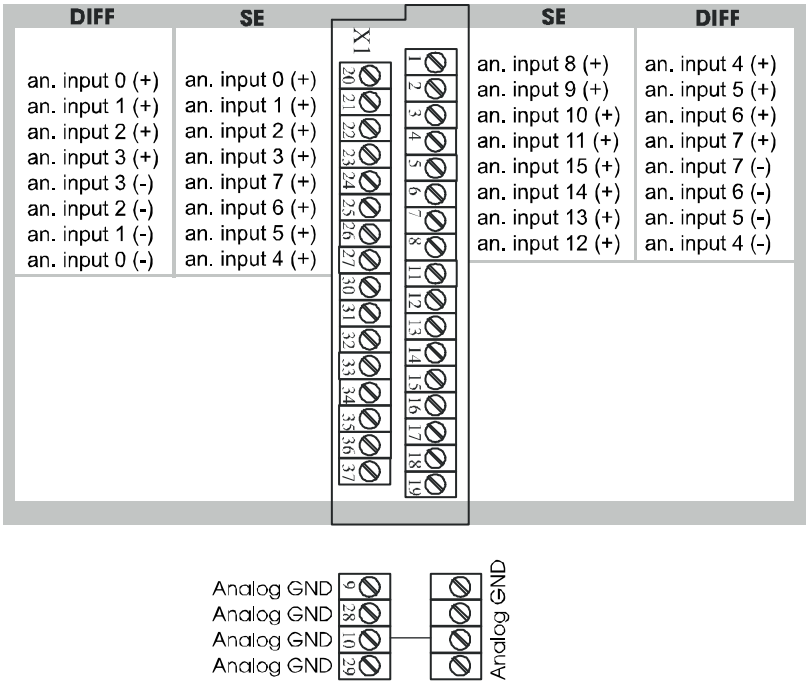
3.3.4 Connection to the APCI-3003

Fig. 3-18: Terminal assignment with the APCI-3003



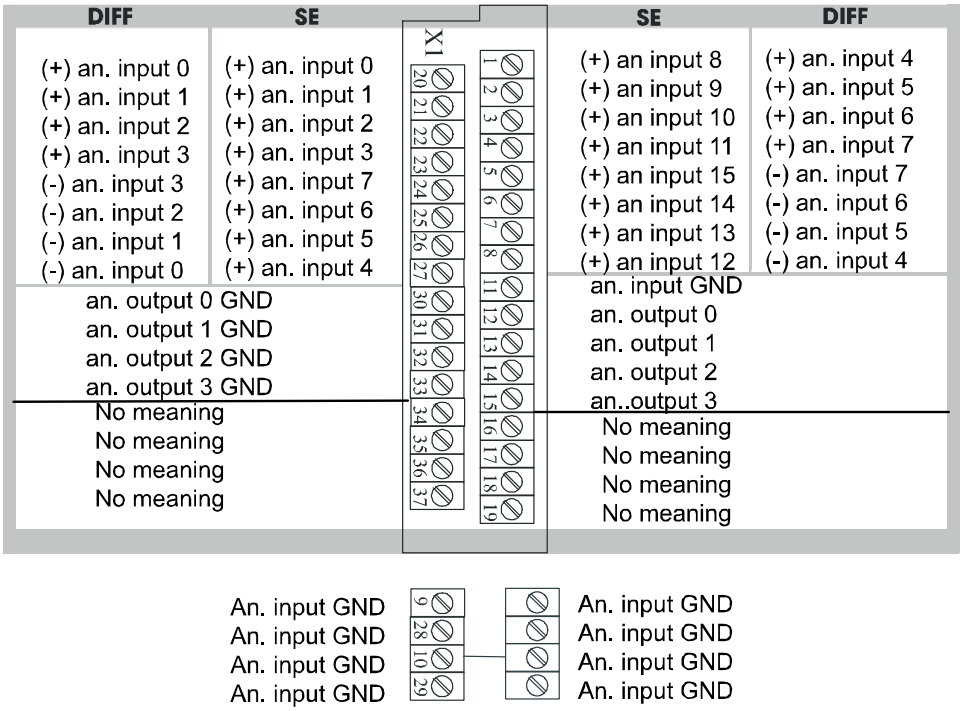
3.3.5 Connection to the APCI-3010 and APCI-3016

Fig. 3-19: Terminal assignment with the APCI-3010 and APCI-3016



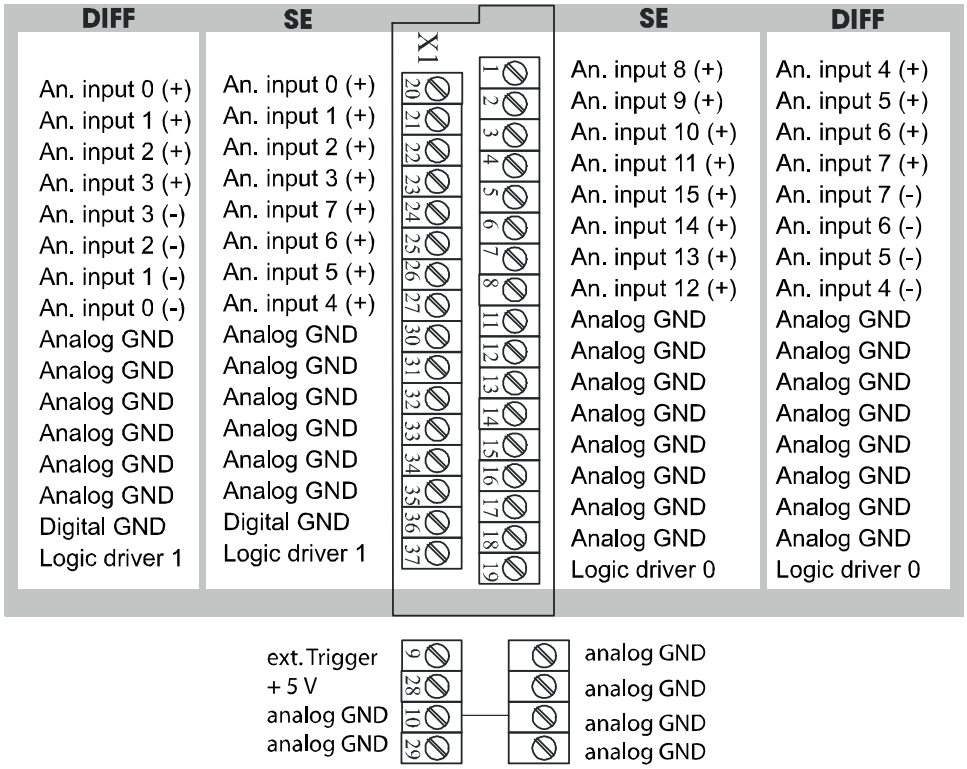
3.3.6 Connection to the APCI-3116

Fig. 3-20: Terminal assignment with the APCI-3116



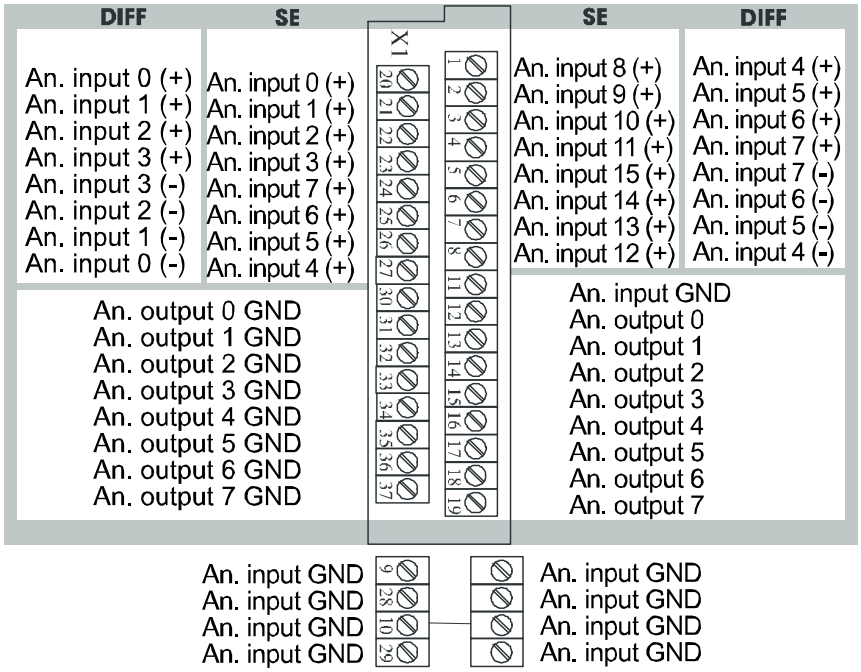
3.3.7 Connection to the PA 302

Fig. 3-21: Terminal assignment with the PA 302



3.3.8 Connection to the PA 311

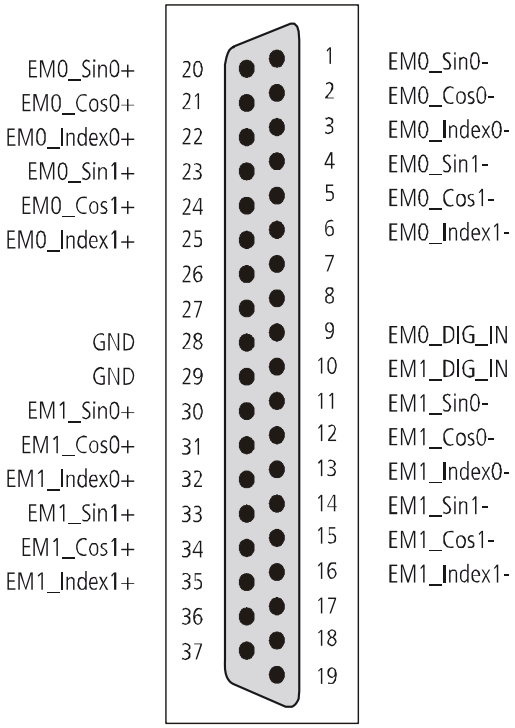
Fig. 3-22: Terminal assignment with the PA 311



3.4 Connection of the PX901-ZG

3.4.1 Connection to the APCIe-1711 (“Sin/Cos” function)

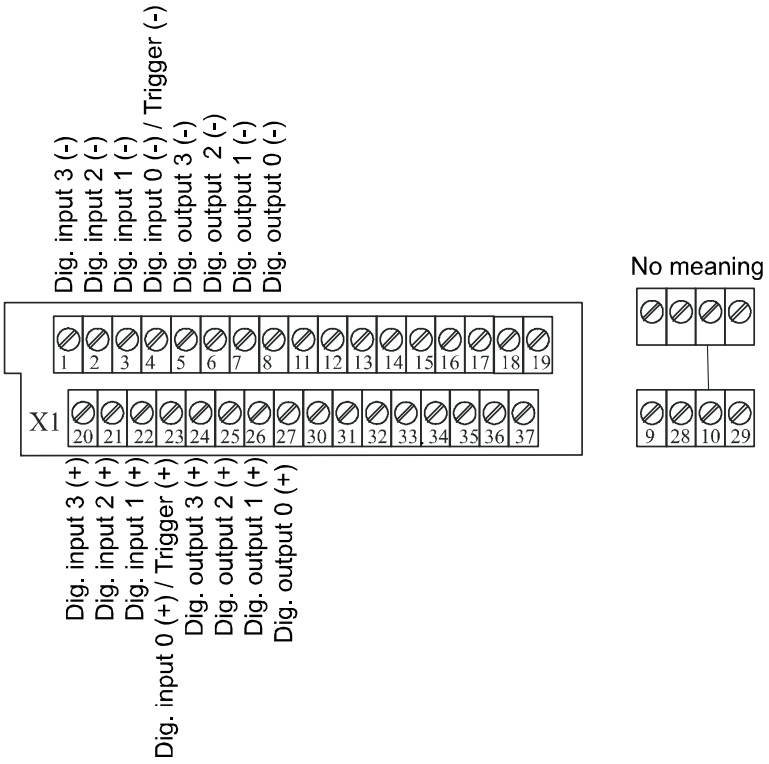
Fig. 3-23: Pin assignment: 37-pin D-Sub male connector (2 EM-SINCOS modules)



EM = Extension module

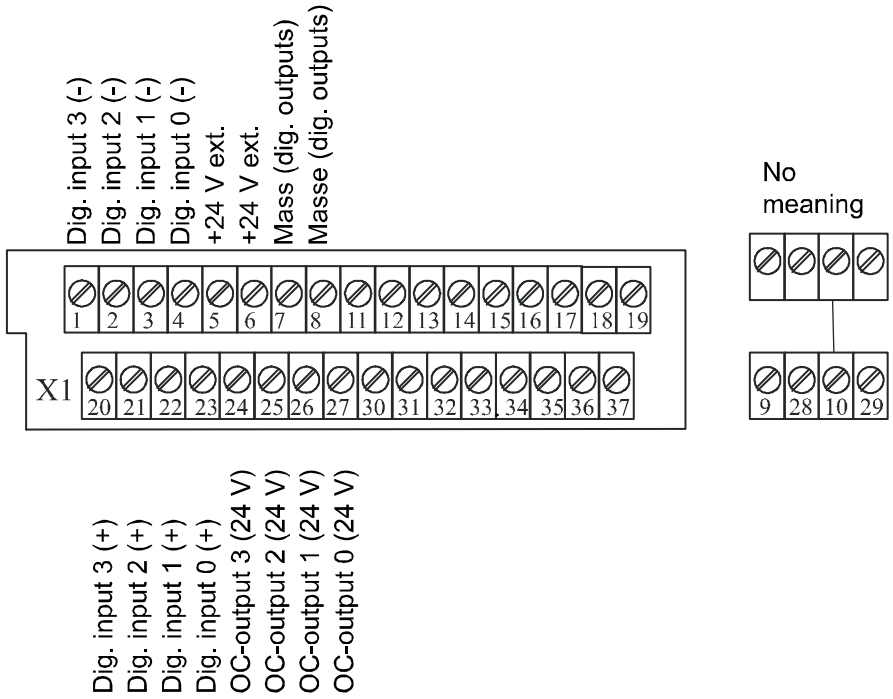
3.4.2 Connection to the APCIe-3021, APCIe-3121, APCIe-3521, APCI-/CPCI-3001, APCI-/CPCI-3120 and CPCIs-3121

Fig. 3-24: Terminal assignment with the APCIe-3021, APCIe-3121, APCIe-3521, APCI-/CPCI-3001, APCI-/CPCI-3120 and CPCIs-3121



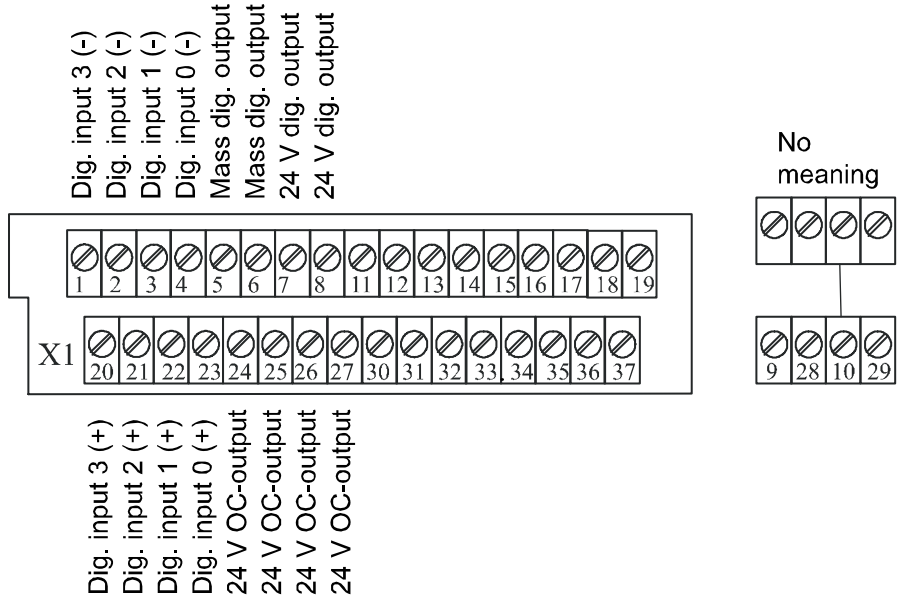
3.4.3 Connection to the APCI-3002

Fig. 3-25: Terminal assignment with the APCI-3002



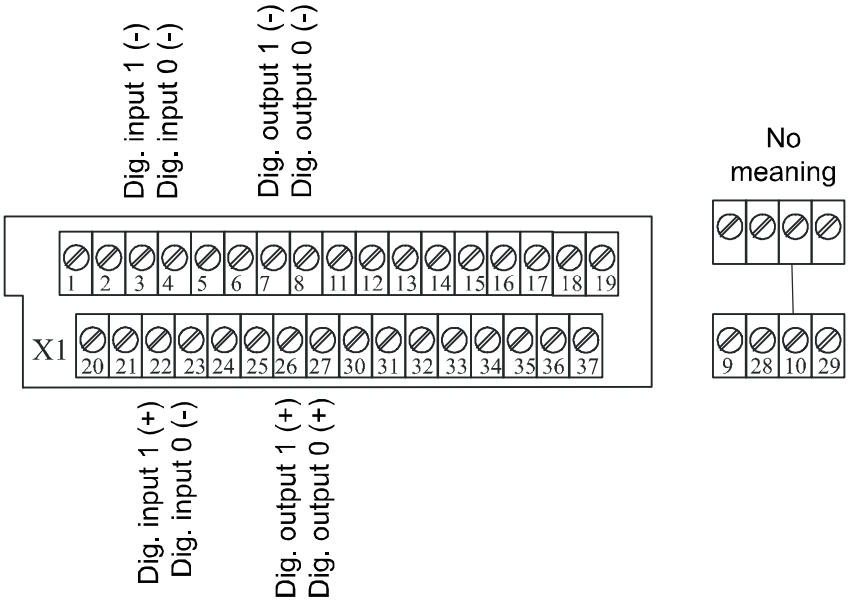
3.4.4 Connection to the APCI-3003

Fig. 3-26: Terminal assignment with the APCI-3003



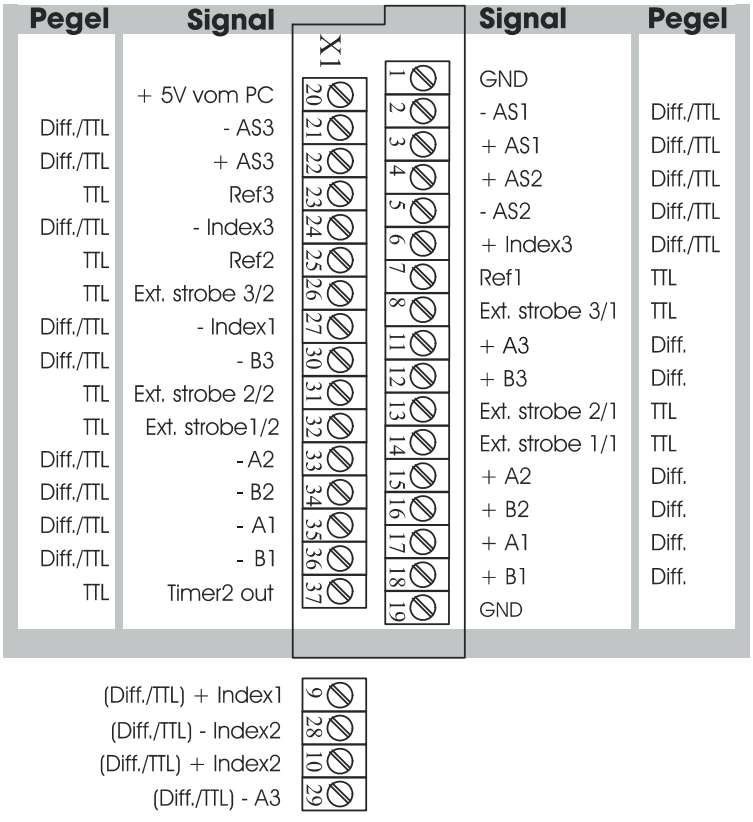
3.4.5 Connection to the APCI-3501

Fig. 3-27: Terminal assignment with the APCI-3501



3.4.6 Connection to the PA 1700-2

Fig. 3-28: Terminal assignment with the PA 1700-2

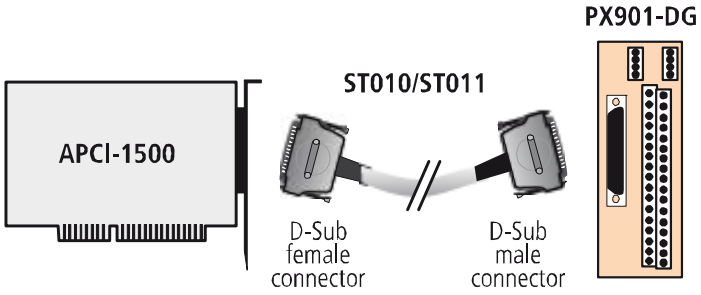


4 Connection examples

4.1 Connection of the PX901-DG

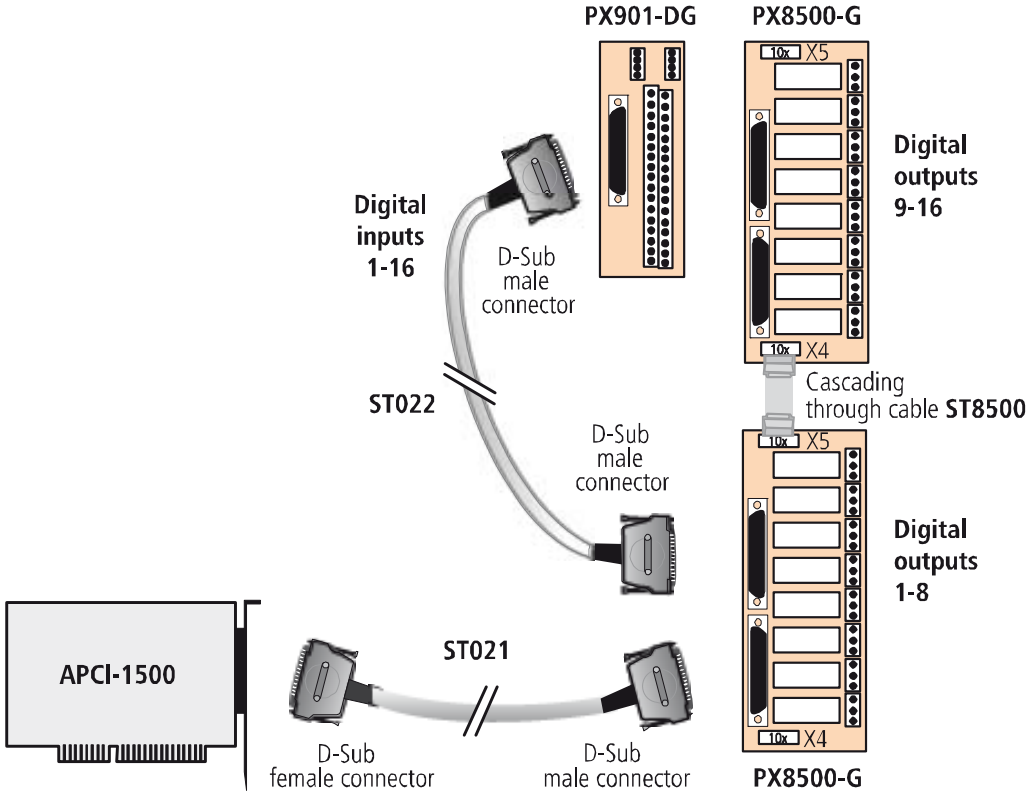
4.1.1 Direct connection

Fig. 4-1: Connection example: Direct connection of the PX901-DG



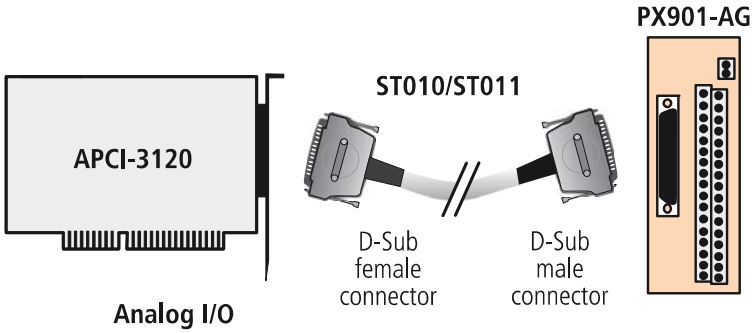
4.1.2 Connection over the relay output board PX8500

Fig. 4-2: Connection example: PX901-DG over relay output board PX8500



4.2 Connection of the PX901-AG

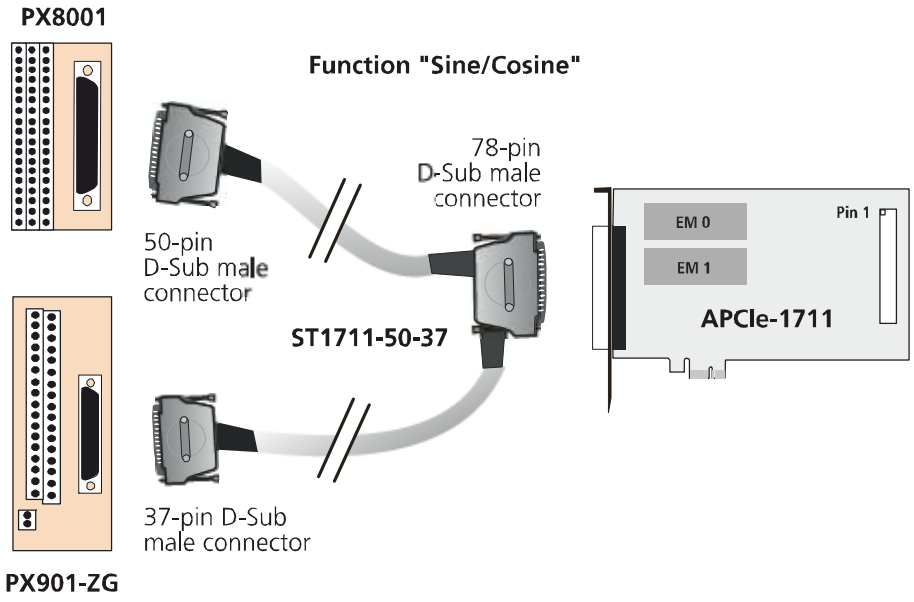
Fig. 4-3: Connection example: PX901-AG



4.3 Connection of the PX901-ZG

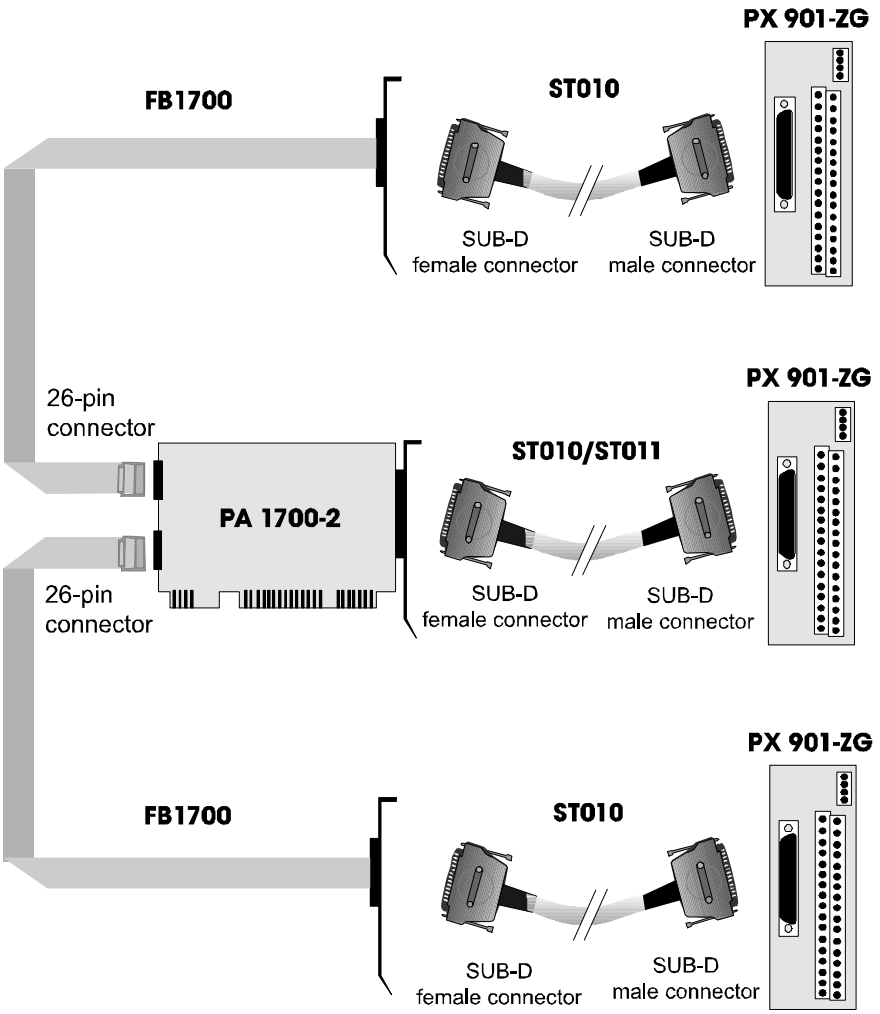
4.3.1 Connection to the counter board APCIe-1711 ("Sin/Cos" function)

Fig. 4-4: Connection example: Counter board APCIe-1711 ("Sin/Cos" function) with PX901-ZG



4.3.2 Connection to the counter board PA 1700-2

Fig. 4-5: Connection example: Counter board PA 1700-2 with PX901-ZG



4.4 Combined connections

4.4.1 Connection to the APCIe-3021, APCIe-3121, APCIe-3521, APCI-/CPCI-3001, APCI-/CPCI-3120 and CPCIs-3121

Fig. 4-6: Connection example 1: PX901-ZG and PX901-AG

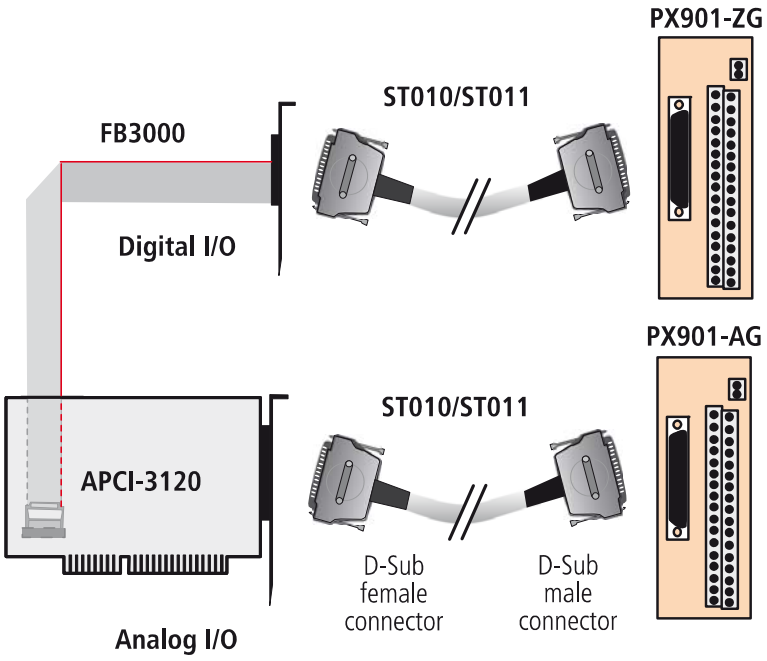
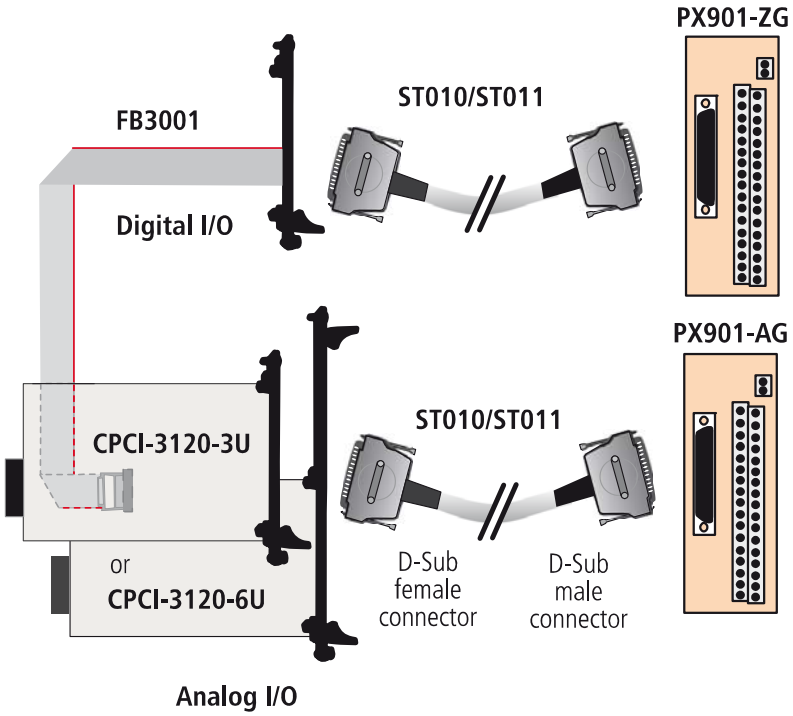
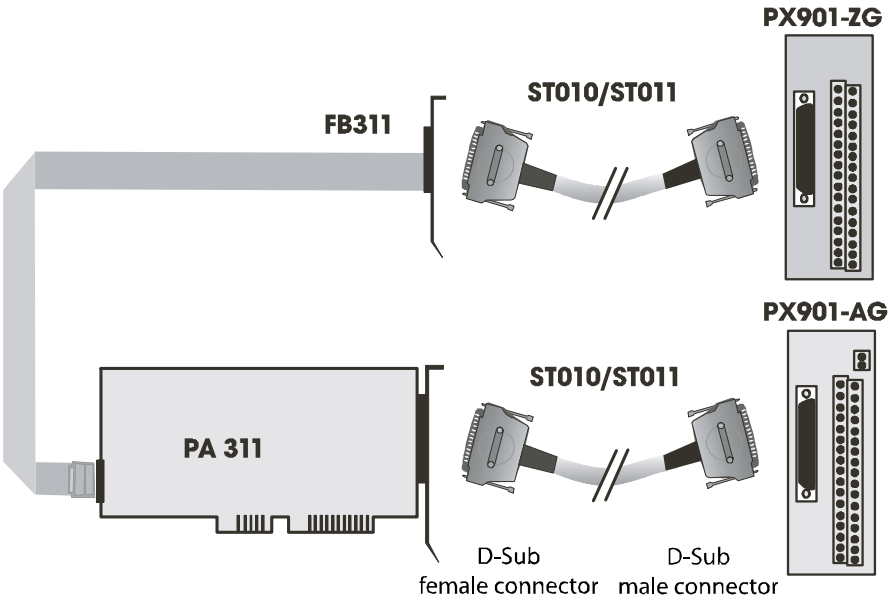


Fig. 4-7: Connection example 2: PX901-ZG and PX901-AG



4.4.2 Connection to the PA 311

Fig. 4-8: Connection example 3: PX901-ZG and PX901-AG



5 Return or disposal

5.1 Return

If you need to return your screw terminal panel, you should read the following checklist before.

Checklist for returning the screw terminal panel:

- Specify the reason for returning your screw terminal panel (e.g. exchange), the serial number of the screw terminal panel, the contact person in your company including his/her telephone extension and e-mail address, as well as the mailing address for a potential new delivery.
  You do not have to indicate the RMA number.

Fig. 5-1: Serial number

- Note down the serial number of the screw terminal panel.
- Place the screw terminal panel in an ESD protective cover. Then pack it in a cardboard box so that it is well-protected for shipping. Send the packed screw terminal panel together with your details to:

  ADDI-DATA GmbH
  Airpark Business Center
  Airport Boulevard B210
  77836 Rheinmünster
  Germany
- If you have any questions, do not hesitate to contact us:

  Phone: +49 7229 1847-0
  E-mail: info@addi-data.com

5.2 Disposal of ADDI-DATA waste equipment

ADDI-DATA organises the disposal of ADDI-DATA products that were put on the German market after 13 August 2005.
If you want to return waste equipment, please e-mail your request to: rohs@addi-data.com.

Screw terminal panels that were delivered after 13 August 2005 can be recognised by the following label:

Fig. 5-2: Disposal: Label

This symbol indicates the disposal of waste electrical and electronic equipment. It is valid in the European Union and in other European countries that have a separate collection system. Products carrying this symbol must not be treated as household waste.
For more detailed information on the recycling of these products, please contact your local citizens' office, your household waste collection service, the shop where you bought this product or the distributor you purchased this product from.

If you dispose of this product correctly, you will help to prevent damage that could be caused to the environment and to human health by inappropriate disposal. The recycling of materials will help to conserve our natural resources.

Disposal in other countries than Germany
Please dispose of the product according to the country-specific regulations.

6 Technical data and limit values

6.1 Electromagnetic compatibility (EMC)

The screw terminal panel **PX901** complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326).
The limit values as set out by the European EMC directive for an industrial environment are complied with.

The respective EMC test report is available on request.

6.2 Mechanical structure

Dimensions (L x W x H):	130 x 70 x 35 mm (without housing) 132 x 87 x 70 mm (with housing)
Connection to peripherals:	
Connector:	37-pin D-Sub female connector (for the connection to the PC board)
Accessories: [1]	see Chapter 3
Cables:	**ST010** (2 m) **ST011** (5 m)

To attach the printed circuit board to a housing rear panel or a mounting plate, four holes for M3 screws are provided.

NOTICE!
The connection lines must be installed in such a way that they are protected against mechanical loads.

6.3 Versions

The screw terminal panel **PX901** is available in the following versions:

Table 6-1: Versions

Version	Features
PX901-D	Screw terminal panel for the connection to digital boards as well as to the MSX-E systems **MSX-E3121** and **MSX-E3701-DIO**, with status display through LEDs, connection via the shielded standard cable **ST010** or **ST011**
PX901-DG	**PX901-D** with housing for mounting on a DIN rail
PX901-A	Screw terminal panel for the connection to analog boards, with overvoltage protection diodes, connection via the shielded standard cable **ST010** or **ST011**
PX901-AG	**PX901-A** with housing for mounting on a DIN rail

[1] not included in standard delivery

Version	Features
PX901-ZG	Screw terminal panel for the connection to the counter boards **APCIe-1711**, **CPCIs-1711** ("Sin/Cos" function) and **PA 1700-2** as well as to the digital I/O of analog boards and the digital inputs of the relay boards **APCIe-2200** and **APCI-2200**, connection via the shielded standard cable **ST010** or **ST011**, with housing for mounting on a DIN rail

The specific version name can be found on the type label at the slot bracket of your board.

6.4 Limit values

NOTICE!
Please observe the limit values of the connected peripherals (PC board, MSX-E system)!

Temperature range:	0-60 °C
Screw terminals:	
Conductor cross-section:	2.5 mm^2 max.
Test/rated load torque:	0.4 Nm

6.4.1 PX-901-D

Status LEDs	
Current consumption:	2.3 mA (at 24 V)
Operating voltage protective circuit	
Varistor (terminals 9, 28)	
Operating voltage:	max. V_{RMS} = 50 V max. V_{DC} = 65 V
Peak pulse power dissipation:	P_{PPM} = 100 mW
Overvoltage protection diode	
Breakdown voltage:	V_{BR} = 37.1 V
Leakage current:	I = 5 µA
Peak pulse power dissipation:	P_{PPM} = 400 W/1 ms

6.4.2 PX901-A

Overvoltage protection	
Overvoltage protection diode	
Breakdown voltage:	V_{BR} = 12.8 V
Leakage current:	I = 5 µA
Peak pulse power dissipation:	P_{PPM} = 600 W/1 ms

7 Appendix

7.1 Glossary

EMC
= Electromagnetic Compatibility

The definition of the VDE regulation 0870 states: Electromagnetic compatibility is the ability of an electrical installation to function satisfactorily within its electromagnetic environment without unduly affecting its environment and the equipment it contains.

ESD
= Electrostatic Discharge

On non-conductive surfaces, an electric charge is conducted away very slowly. If the dielectric strength is overcome, there is a fast potential equalisation between the surfaces involved. The often very sudden equalisation process is referred to as electrostatic discharge (ESD). Currents of up to 20 A may occur in this process.

Limit value
Exceeding the limit values, even for a short time, can easily result in the destruction of the component or the (temporary) loss of functionality.

Operating voltage
The operating voltage is the voltage to the device in sustained operation. It must not exceed the maximum sustained voltage, and all unfavourable operating conditions, such as possible mains power surges for over a minute when the device is switched on, must be taken into account.

7.2 Index

8 Contact and support

Do you have any questions? Write or phone us:

Address: ADDI-DATA GmbH
Airpark Business Center
Airport Boulevard B210
77836 Rheinmünster
Germany

Phone: +49 7229 1847-0
Fax: +49 7229 1847-222

E-mail: info@addi-data.com

Manual and software download from the Internet:

www.addi-data.com